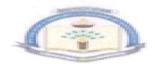


Shri Vile Parle Kelavani Mandal's DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING (Autonomous College Affiliated to the University of Mumbai)



NAAC Accredited with "A" Grade (CGPA: 3.18)

REGULAR EXAMINATION TIMETABLE, MAY, 2025 BACHELOR OF TECHNOLOGY (B. Tech.)

THIRD YEAR, SEMESTER - VI (For 2024-25 Batch Under Autonomy)

Timings: 09:00 a.m. To 11:00 a.m.

Date: 5th April, 2025

									Date: 5 April, 2025
Day & Date		Mechanical Engineering	Electronics & Telecommunication Engg	Information Technology	Computer Engineering	Computer Science & Engineering (Data Science)	Artificial Intelligence (AI) and Data Science	Artificial Intelligence and Machine Learning	Computer Science and Engineering (IOT and Cyber Security withBlock Chain Technology)
Tuesday, 20/05/2025		Design of Machine Elements	Digital Communication	Software Engineering	Software Engineering and Project Management	Natural Language Text Processing	Optimization Techniques	Dev Ops and ML Ops	Secure Software Engineering
Thursday, 22/05/2025		Finite Element Analysis	Radiating Systems	Machine Learning		Machine Learning - III (Reinforcement Learning)	Deep Learning		Data Engineering
Saturday, 24/05/2025		Control Systems	Fundamentals of Digital Image Processing	Image Processing and Computer Vision	Information Security	Image Processing and Computer Vision - II	Computer Vision	Computer Vision	Big Data Analytics
Tuesday, 27/05/2025		Creative Engineering Design	Basic VLSI	Infrastructure Security	- Business Analytics	Cloud Computing	Computational Finance	Time Series Analysis	Machine Learning
		Machine Learning	Big Data Analytics	Augmented Reality and Virtual Reality		Computational Neuroscience			
		Refrigeration and Air- conditioning	Neural Network and Fuzzy Logic	Information Systems and IT Governance	Compiler Design	Recommender System	MLOps	Human Machine Interaction	Mobile Device Security and Forensics
		Mechatronics	Linear Algebra						
			Operating Systems						
Thursday, 29/05/2025	Honours	Electric Drives and Control	Concepts and Algorithms of Artificial Intelligence & Machine Learning	DevOps	Ubiquitous Computing	Financial Market and Computing	Algorithms for Computational Biology	Augmented Reality and Virtual Reality	Cognitive Computing
		Modelling and Design of Robotics	IoT System Design						
	Minors	Machine Learning	Electric Drives and Control	Modelling and Design of Robotics	Electric Drives and Control	Modelling and Design of Robotics	IoT System Design	IoT System Design	Machine Learning
		Machine Learning	Modelling and Design of Robotics						
Saturday, 31/05/2025		-	Computer Networks	Big Data Analytics		-		ş	Universal Human Values

Important Note: Changes if any, in the time table shall be communicated on the college website (www.djsce.ac.in).

Controller of Examinations

Dr. Hari Vasudevan

Principal